

ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18

Stylesheet Version v18.0

Title of Invention

Method and apparatus for enabling a low power mode for a
processor

Application Number : 10/027939



Confirmation Number: 2581

First Named Applicant: Xia Dai

Attorney Docket Number: 42P9738

Art Unit: 2116

Examiner: Stefan Stoyanov

Search string: (6052789 or 5632038 or 5715465).pn

Certification: This Information Disclosure Statement was submitted under the following conditions, which satisfies the requirement under 37 CFR 1.97(e). The filer certified:

That each item of information contained in the information disclosure statement was first cited in any communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of the information disclosure statement.

US Patent Documents

Note: Applicant is not required to submit a paper copy of cited US Patent Documents

init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass
	1	6052789	2000-04-18	Lin			
	2	5632038	1997-05-20	Fuller			
	3	5715465	1998-02-03	Savage et al.			

Signature

Examiner Name	Date